# Exercise: Specification and Verification Using Set Operations and BDDs 

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## About Myself

## Jiahui Xu

- PhD student in the DYNAMO group since April 2022

Research topics that I am interested:

- High-level synthesis (HLS): compile C/C++ code into digital circuits
- Optimization and formal verification of HLS-produced circuits

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## Event Reminder: Student Meets Lab

- Introduce to you our theses and projects
- Next Tuesday, 05 December 2023
- From 17:30 to 19:00 in the ETZ Foyer (ETZ E90.1)

First half of today's lecture
We have four exercise sessions:

- 30.11.2023: set operations, characteristic functions, BDDs
- 07.12.2023: reachability and temporal logic
- 14.12.2023: Petri nets
- 21.12.2023: time Petri nets


## Q1.1 Set Operations and Characteristic Functions

## Program states classified in sets

- X: Set of all states
- N: Set of normal states
- E: Set of error states
- O: Set of states with memory overflow



## Q1.1 Set Operations and Characteristic Functions

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- X: Set of all states
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(a) What is the characteristic function* $\Psi_{\mathrm{x}}$ of the set of all states $X$ ?

$$
\forall s \in X, \Psi \times(\sigma(\mathrm{s})))=1
$$

Because state $s$ is always in the set of all states.

Sets and Relations

- Representation of a subset $A \subseteq E$ :
- Binary encoding $\sigma(e)$ of all elements $e \in E$
- Subset $A$ is represented by $a \in A \Leftrightarrow \psi_{A}(\sigma(a))$
- Stepwise construction of the BDD corresponding to some subsets.

$$
\begin{aligned}
& c \in A \cap B \Leftrightarrow \\
& c \in A \cup B \Leftrightarrow \\
& \psi_{A}(\sigma(c)) \cdot \psi_{B}(\sigma(c)) \\
& c \in A \backslash B \Leftrightarrow \\
& c \in E \backslash A \Leftrightarrow \\
&c \mid(\sigma))+\psi_{A}(\sigma(c)) \cdot \overline{\psi_{B}(\sigma(c))} \\
& c(\sigma(c))
\end{aligned}
$$

## Program states classified in sets

- X: Set of all states
- N: Set of normal states
- E: Set of error states

The union of two sets $A$ and $B$ is the set of elements which are in $A$, in $B$, or in both $A$ and $B .{ }^{[2]}$ In set-builder notation,

- O: Set of states with memory overflow
$A \cup B=\{x: x \in A$ or $x \in B\}{ }^{[3]}$
The intersection of two sets $A$ and $B$, denoted by $A \cap B_{i}{ }^{[3]}$ is the set of all objects that are members of both the sets $A$ and $B$. In symbols:

$$
A \cap B=\{x: x \in A \text { and } x \in B\} .
$$

(b) "Each state (in the set of all states) is either a normal or an error state or both".

Express this property in terms of sets and characteristic functions

$$
X=N \cup E
$$

$$
\Psi_{\mathrm{x}}=1=\Psi_{\mathrm{N}}+\Psi_{\mathrm{E}}
$$

## Program states classified in sets

- X: Set of all states
- N : Set of normal states
- E: Set of error states
- O: Set of states with memory overflow
(c) "If a state (in the set of all states) is in the set of overflow states, then it is not a normal state".
Express this property in terms of sets and characteristic functions.
(d) Describe Q1, the set of error states which are not an overflow,
- Stepwise construction of the BDD corresponding to some subsets.

| $c \in A \cap B$ | $\Leftrightarrow$ |
| ---: | :--- |
| $c \in A \cup B$ | $\psi_{A}(\sigma(c)) \cdot \psi_{B}(\sigma(c))$ |
| $c \in A \backslash B$ | $\Leftrightarrow$ |
| $c$ | $\psi_{A}(\sigma(c))+\psi_{B}(\sigma(c))$ |
| $c \in E \backslash A$ | $\Leftrightarrow$ |
| $c$ | $\overline{\psi_{A}(\sigma(c))}$ |

$c \in A \backslash B \quad \Leftrightarrow \quad \psi_{A}(\sigma(c)) \cdot \overline{\psi_{B}(\sigma(c))}$
$c \in E \backslash A \quad \Leftrightarrow \quad \overline{\psi_{A}(\sigma(c))}$

Your turn! in terms of sets and characteristic functions.
(e) Describe Q2, the set of states that satisfies $\mathrm{O} \Rightarrow \mathrm{E}$, i.e., the set of states for which this property holds, in terms of sets and characteristic functions.
Hint: $\mathrm{O} \Rightarrow \mathrm{E}$ reads O implies E , in other words, if a state is in O , then it is in E .

## Sets and Relations

- Binary encoding $\sigma(e)$ of all elements $e \in E$
- Subset $A$ is represented by $a \in A \Leftrightarrow \psi_{A}(\sigma(a))$


## Program states classified in sets

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(c) "If a state (in the set of all states) is in the set of overflow states, it is

| $c \in A \cap B$ | $\Leftrightarrow$ |
| ---: | :--- |
| $c \in A \cup B$ | $\psi_{A}(\sigma(c)) \cdot \psi_{B}(\sigma(c))$ |
| $c \in A \backslash B$ | $\Leftrightarrow$ |
| $c \in \psi_{A}(\sigma(c))+\psi_{B}(\sigma(c))$ |  |
| $c \in E \backslash A$ | $\Leftrightarrow$ |
|  | $\Leftrightarrow \overline{\psi_{A}(\sigma(c))} \cdot \overline{\psi_{B}(\sigma(c))}$ |

$c \in A \cup B \quad \Leftrightarrow \quad \psi_{A}(\sigma(c))+\psi_{B}(\sigma(c))$
$c \in E \backslash A \quad \Leftrightarrow \quad \overline{\psi_{A}(\sigma(c))}$

- Stepwise construction of the BDD corresponding to some subsets.

Express this property in terms of sets and characteristic functions.

For every state s: if " $s$ is in set $O$ " then " $s$ is not in set N"

## Convention: if A then B

## Let's agree with the following convention...

When we test a specification "if A then B", we are actually executing the following program:

```
// if A is true, then
if (A){
    // check if B is true
    assert(B);
}
// the execution is ok
return(0);
```

Conclusion: "if A then B" is the same as "(not A) or B"

## Convention: if A then B



From Wikipedia, the free encyclopedia
For other uses, see Material implication (disambiguation).
Not to be confused with Material inference

This article may be too technical for most readers to understand. Please help improve it to make it understandable to non-experts, without removing the technical details. (December 2018) (template removal help)

In propositional logic, material implication ${ }^{[1][2]}$ is a valid rule of replacement that allows for a conditional statement to be replaced by a disjunction in which the antecedent is negated. The rule states that $P$ implies $Q$ is logically equivalent to not- $P$ or $Q$ and that either form can replace the other in logical proofs. In other words, if $P$ is true, then $Q$ must also be true, while if $Q$ is not true, then $P$ cannot be true either; additionally when $P$ is not true, $Q$ may be either true or false

$$
P \rightarrow Q \Leftrightarrow \neg P \vee Q
$$

Where " $\Leftrightarrow$ " is a metalogical symbol representing "can be replaced in a proof with," $P$ and $Q$ are any given logical statements, and $\neg P \vee Q$ can be read as "(not $P$ ) or $Q$ ". To illustrate this, consider the following statements:

- $P$ : Sam ate an orange for lunch
- $Q$ : Sam ate a fruit for lunch

Then, to say, "Sam ate an orange for lunch" implies "Sam ate a fruit for lunch" ( $P \rightarrow Q$ ). Logically, if Sam did not eat a fruit for lunch, then Sam also cannot have eaten an orange for lunch (by contraposition). However, merely saying that Sam did not eat an orange for lunch provides no information on whether or not Sam ate a fruit (of any kind) for lunch.

I didn't invent this concept!

Material implication

| Type | Rule of replacement |
| :--- | :--- |
| Field | Propositional calculus |
| Statement | $P$ implies $Q$ is logically equivalent |
|  | to not- $P$ or $Q$. Either form can <br> replace the other in logical proofs. |
| Symbolic <br> statement | $P \rightarrow Q \Leftrightarrow \neg P \vee Q$ |

Transformation rules
Propositional calculus Rules of inference Implication introduction / elimination (modus ponens) Biconditional introduction / elimination Conjunction introduction / elimination Disjunction introduction / elimination Disiunctive / hvoothetical svllooism

## Sets and Relations

- Binary encoding $\sigma(e)$ of all elements $e \in E$
- Subset $A$ is represented by $a \in A \Leftrightarrow \psi_{A}(\sigma(a))$
- Stepwise construction of the BDD corresponding to some subsets.

| $c \in A \cap B$ | $\Leftrightarrow$ |
| ---: | :--- |
| $c \in A \cup B$ | $\Leftrightarrow$ |
| $\psi_{A}(\sigma(c)) \cdot \psi_{B}(\sigma(c))$ |  |
| $c \in A \backslash B$ | $\Leftrightarrow$ |
| $c \in \psi_{A}(\sigma(c))+\psi_{B}(\sigma(c))$ |  |
| $c \in \overline{\psi_{B}(\sigma(c))}$ |  |
|  | $\Leftrightarrow$ |

"if $A$ then $B$ " is the same as " $(\operatorname{not} A)$ or $B$ '

## Program states classified in sets

- X: Set of all states
- N : Set of normal states
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- O: Set of states with memory overflow
(c) "If a state (in the set of all states) is in the set of overflow states, it is not a normal state".
Express this property in terms of sets and characteristic functions.
For every state s: if "s is in set O " then "s is not in set N"
For every state s: not ("s is in set O" and "s is in set N")
Set of states violates this property: $\bar{X}=0 \cap N$

$$
\Psi_{X}=\overline{\Psi_{O} \cdot \Psi_{N}}
$$

Set of all states: $\mathrm{X}=\overline{O \cap N}$

Q1.1 Set Operations and Characteristic Functions
Sets and Relations

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\end{aligned}
$$

"if $A$ then $B$ " is the same as " $(\operatorname{not} A)$ or $B$ '

- E: Set of error states
(d) Describe Q1, the set of error states which are not an overflow, in terms of sets and characteristic functions.


## Program states classified in sets

- X: Set of all states
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For each state $x$ in Q1: " $x$ is not in set $O$ " and " $x$ is in set $E$ "
Set of states satisfies this property: $Q_{1}=E \backslash 0$

$$
\psi_{Q_{1}}=\psi_{E} \cdot \overline{\psi_{O}}
$$

## Sets and Relations

- Representation of a subset $A \subseteq E$ :
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& \psi_{A}(\sigma(c))
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## Program states classified in sets

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(e) Describe Q2, the set of states that satisfies $\mathrm{O} \Rightarrow \mathrm{E}$, i.e., the set of states for which this property holds, in terms of sets and characteristic functions.
Hint: $\mathrm{O} \Rightarrow \mathrm{E}$ reads O implies E , in other words, if a state is in O , then it is in E .


## The same problem as (c). <br> What happened for all states that are not in O?

## Q1.2 Specifications in Boolean Encoding

Bus
"if $A$ then $B$ " is the same as "(not $A)$ or $B$ "


Sensor nodes
We can use binary variables to indicate the state of the system:

- $x_{1}=1$ : node 1 is using the bus
- $x_{2}=1$ : node 2 is using the bus
- $x_{3}=1$ : node 3 is using the bus
- $\mathrm{X}_{\mathrm{s}}=1$ : the sink is awake
- $\mathrm{Xb}_{\mathrm{b}}=1$ : the system is bootstrapping

Sensor network
(C1) When one or more nodes are using the bus, the sink must be awake to receive data.
(C2) No more than one node can use the bus at the same time.
(C3) When the network is in bootstrapping mode, then the sink must be awake, and the nodes cannot use the bus.

> Your turn! Describe (1) the 3 constraints using the
> binary variables, and (2) the complete specification

## Q1.2 Specifications in Boolean Encoding

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Sensor network
(C1) When one or more nodes are using the bus, the sink must be awake to receive data.
If "one or more nodes are using the bus" then "the sink is awake"
Not ("not (no node is using the bus)" and "the sink is not awake")


## Q1.2 Specifications in Boolean Encoding

Bus
"if A then B " is the same as "( $\operatorname{not} \mathrm{A})$ or B "


Sensor nodes
We can use binary variables to indicate the state of the system:

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- $\mathrm{Xb}=1$ : the system is bootstrapping

Sensor network
(C2) No more than one node can use the bus at the same time.
"exactly one node is using the bus" or "no node is using the bus"

$$
x_{1} \cdot \overline{x_{2}} \cdot \overline{x_{3}}+\overline{x_{1}} \cdot x_{2} \cdot \overline{x_{3}}+\overline{x_{1}} \cdot \overline{x_{2}} \cdot x_{3}+\overline{x_{1}} \cdot \overline{x_{2}} \cdot \overline{x_{3}}
$$

## Q1.2 Specifications in Boolean Encoding



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- $X_{s}=1$ : the sink is awake
- $\mathrm{Xb}_{\mathrm{b}}=1$ : the system is bootstrapping

Sensor network
(C3) When the network is in bootstrapping mode, then the sink must be awake, and the nodes cannot use the bus.

If "the system is bootstrapping" then ("the sink is awake" and "no nodes is using the bus")
Not ("the system is bootstrapping" and ("the sink is not awake" or "one or more nodes is using the bus"))

## Q2.1: Combinational Equivalence Checking (CEC) Using BDDs

## We want to check that the circuit implements the functionality of F .

$$
F_{1}:=x_{1} \overline{x_{2}}+x_{1} x_{3}+\overline{x_{2}} x_{3}+\overline{x_{1}} x_{2} \overline{x_{3}}
$$



Can we just test the two circuits with all possible inputs? Not scalable for large designs!
For a 64-bit input circuit. It takes about $\sim 70$ years to verify the design, assuming we have a 2 GHz CPU, that checks one input in every clock cycle

ROBDD-based method has linear complexity (in the best case) with respect to the number of variables

## Q2.1: Combinational Equivalence Checking (CEC) Using BDDs

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$$



## Q2.1: Combinational Equivalence Checking (CEC) Using BDDs



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## We want to check that the circuit implements the functionality of F1.

$$
F_{1}:=x_{1} \overline{x_{2}}+x_{1} x_{3}+\overline{x_{2}} x_{3}+\overline{x_{1}} x_{2} \overline{x_{3}}
$$


(a) Derive the Boolean function of the circuit diagram:

$$
\overline{\overline{x_{1}+x_{2}+x_{3}}+\overline{x_{1}+\overline{x_{2}}+\overline{x_{3}}}+\overline{\overline{x_{1}}+\overline{x_{2}}+x_{3}}}
$$

(b) Draw the ROBDD of F1 (evaluation order $x 1<$ $x 2<x 3$ ).
(c) Draw the ROBDD of F2, are the two functions identical?

## Q2.2: ROBDDs with Different Orderings

$$
G\left(x_{1}, x_{2}, y_{1}, y_{2}\right):=\left(x_{1} \leftrightarrow y_{1}\right) \cdot\left(x_{2} \leftrightarrow y_{2}\right)
$$

(a) Compute the Boole-Shannon expansion of G , with respect to the following ordering:

$$
x_{1}<x_{2}<y_{1}<y_{2}
$$

(b) Draw the ROBDD of the function above, with the following two variable orderings:

$$
x_{1}<x_{2}<y_{1}<y_{2} \quad x_{1}<y_{1}<x_{2}<y_{2}
$$

Which of the following have fewer decision nodes?

## Q2.2: ROBDDs with Different Orderings

$$
G\left(x_{1}, x_{2}, y_{1}, y_{2}\right):=\left(x_{1} \leftrightarrow y_{1}\right) \cdot\left(x_{2} \leftrightarrow y_{2}\right)
$$

(a) Boole-Shannon expansion of G :

$$
g=x_{1}\left\{y_{1}\left[x_{2}\left(y_{2}\right)+\overline{x_{2}}\left(\overline{y_{2}}\right)\right]+\overline{y_{1}}[0]\right\}+\overline{x_{1}}\left\{y_{1}[0]+\overline{y_{1}}\left[x_{2}\left(y_{2}\right)+\overline{x_{2}}\left(\overline{y_{2}}\right)\right]\right\}
$$

(b) ROBDDs:


$$
x_{1}<x_{2}<y_{1}<y_{2}
$$


$x_{1}<y_{1}<x_{2}<y_{2}$

