1 Specifications in Computation Tree Logic (CTL)

a) $\text{AG} (\text{valid} \rightarrow \text{AF ready})$

b) $\text{AG AF} (\text{ready})$

c) $\text{AG} ((\text{valid} \land \neg \text{ready}) \rightarrow \text{AX valid})$
2 Model Checking CTL Specifications (I)

a) \( Q = \{0, 1, 2, 3\} \)
b) \( Q = \{0, 3\} \)
c) \((\text{AX} \ a)\) holds for \(\{2, 3\}\), thus \( Q = \{1, 2\} \)
d) \((a \land \text{EX} \ \neg a)\) is true for states where \(a\) is true and there exists a direct successor for which it is not. Only state 0 satisfy this (from it you can transition to 1, where \(a\) does not hold). Moreover, state 0 is reachable for all states in this state machine ("from all states there exists a path going through 0 at some point"). Hence \( Q = \{0, 1, 2, 3\} \)

3 Model Checking CTL Specifications (II)

a) \( \neg \text{AF} \ Z \equiv \text{EG} \ \neg Z \)

b) The goal is to compute \([\neg \text{EG} \ \neg Z]\); we use the following procedure:

\[
\begin{align*}
Q_0 &= S \setminus \ Z \\
Q_{i+1} &= Q_i \cap \text{Pre}(Q_i, R) \\
k &= \min\{i \mid Q_{i+1} = Q_i\}
\end{align*}
\]

\([\text{AF} \ Z] = S \setminus Q_k\)

The set of states \( Q_k = [\text{EG} \ \neg Z] \) is obtained when the procedure discovered a fixed-point (i.e., it finds a value of \(k\) such that \(Q_k = Q_{k+1}\)). The final solution is obtained by taking a negation: \([\text{AF} \ Z] = S \setminus Q_k\).

The main idea is that we start with the states that are not in \(Z\). Then, at each iteration, we create an intersection between the current set of states, and all predecessors from which we can reach one of the states in the set. By doing this, we will remove any states from which there exists some future, in which \(Z\) does not hold. We stop the iteration once nothing changes anymore (we define \(k\) to be the first index for which the set of states remains the same). Hence, we express have \( Q_k = [\text{EG} \ \neg Z] \). What is left to do is to negate the final set (every state which is not present in \(Q_k\)).

c) We translate the procedure above directly into an algorithm:

Require: \(\psi_Z, \psi_R\)

\[
\begin{align*}
\psi_{\text{cur}} &\leftarrow \neg \psi_Z \\
\psi_{\text{next}} &\leftarrow \psi_{\text{cur}} \land \psi_{\text{pre}(\psi_{\text{cur}}, R)} \\
\text{while } &\psi_{\text{cur}} \neq \psi_{\text{next}} \text{ do} \\
&\psi_{\text{cur}} \leftarrow \psi_{\text{next}} \\
&\psi_{\text{next}} \leftarrow \psi_{\text{cur}} \land \psi_{\text{pre}(\psi_{\text{cur}}, R)} \\
\text{end while} \\
\text{return } &\psi_{\text{AF} \ Z} = \neg \psi_{\text{cur}}
\end{align*}
\]
4 Sequential Equivalence Checking

a) 
\[ \psi_A(x_A, x'_A, u) = \neg x_A \neg x'_A \neg u + \neg x_A x'_A u + x_A x'_A \neg u \]
\[ \psi_B(x_B, x'_B, u) = \neg x_B \neg x'_B \neg u + \neg x_B x'_B u + x_B x'_B \neg u + x_B \neg x'_B u \]

b) 
\[ \psi_f(x_A, x'_A, x_B, x'_B) = (\neg x_A x'_A + x_A x'_A) \cdot (\neg x_B x'_B + x_B \neg x'_B) + (\neg x_A x'_A + x_A x'_A) \cdot (\neg x_B \neg x'_B + x_B x'_B) \]
\[ \quad = \neg x_A x'_A \neg x_B x'_B + \neg x_A x'_A x_B x'_B + x_A x'_A \neg x_B x'_B + x_A x'_A x_B \neg x'_B \]
\[ \quad + \neg x_A x'_A \neg x_B x' + \neg x_A x'_A x_B x' + x_A x'_A \neg x_B x' + x_A x'_A x_B \neg x' \]

\[ \psi_x(x_A, x_B) = \neg x_A x_B + x_A x_B + x_A x_B + \neg x_A x_B \]

\[ \Rightarrow \psi(x_A, x_B) = \neg x_A x_B + x_A x_B + x_A x_B + \neg x_A x_B \]

e) From the reachable output function, we see that these state machines are not equivalent. Indeed, there exists a reachable output admissible \((\psi_y((y_A, y_B) = (0, 1)) = 1)\) for which \(y_A \neq y_B\).
Another way of looking at it: \(\psi_y \cdot (y_A \neq y_B) \neq 0\) where \((y_A \neq y_B) = \neg y_A y_B + y_A \neg y_B\).