Discrete Event Systems
Exercise 5

1 Counter Automaton

A push-down automaton is basically a finite automaton augmented by a stack. Consider a finite automaton that (instead of a stack) has an additional counter $C$, i.e., a register than can hold a single integer of arbitrary size. Initially, $C = 0$. We call such an automaton a Counter Automaton $M$. $M$ can only increment or decrement the counter, and test it for 0. Since theoretically, all possible data can be coded into one single integer, a counter automaton has unbounded memory. Further, let $L_{\text{count}}$ be the set of languages recognized by counter automata.

- Let $L_{\text{reg}}$ be the set of regular languages. Prove that $L_{\text{reg}} \subseteq L_{\text{count}}$.
- Prove that the opposite is not true, that is, $L_{\text{count}} \not\subseteq L_{\text{reg}}$. Do so by giving a language which is in $L_{\text{count}}$, but not in $L_{\text{reg}}$. Characterize (with words) the kind of languages a counter automaton can recognize, but a finite automaton cannot.
- Which automaton is stronger? A counter automaton or a push-down automaton? Explain your decision.

2 Push Down Automaton

For each of the following languages, draw a PDA (if possible deterministic) that accepts $L$.

a) $L = \{a^i b^j a^i b^j \mid i, j > 0\}$

b) $L = \{u \mid u \in \{0,1\}^*, \text{ and } u_{\text{reverse}} = u\}$

c) $L = \{u \mid u \in \{0,1\}^*, \text{ and } u_{\text{reverse}} \neq u\}$

3 Context Free Grammars

For each of the following languages give a CFG describing $L$.

a) $L = \{x#y \mid x, y \in \{a,b\}^*, \text{ and } x \text{ is not a permutation of } y\}$

b) $L = \{x#y \mid x, y \in \{a,b\}^*, \text{ and } x \neq y\}$

4 Tandem Pumping

For the following languages, determine whether they are context free or not.

a) $L = \{a^i b^j c^k \mid 0 < i < j < k\}$

b) $L = \{x \mid x \in \{0,1\}^*, \text{ and } x \text{ contains an even number of } '0' \text{ and an even number of } '1'\}$

c) $L = \{x#y \mid x, y \in \{0,1\}^*, \text{ and } x \text{ is a permutation of } y\}$
5 Transducer and Turing Machine

Alice is very happy because she was accepted for an internship at Tintel, one of the world’s leading processor manufacturers. Unfortunately, she has only attended the famous DES lecture during her studies at ETH and knows nothing about electronic circuits. Therefore, she wants to solve her first assignment using a transducer - please assist her:

a) Alice first needs to compute the sum \( a + b \) of two binary numbers \( a \) and \( b \). The numbers are represented bitwise with LSB first. I.e. \( a \) is given as \( a[0]a[1]a[2]a[3] \ldots a[n] \) and \( b \) as \( b[0]b[1]b[2] \ldots b[n] \). Unfortunately, Alice receives a single string containing both, \( a \) and \( b \) mixed together in the form \( a[0]b[0]a[1]b[1]a[2]b[2] \ldots a[n]b[n] \). Draw the transducer that outputs \( a + b \) (in binary) with same bit ordering (LSB first) and only accepts upon a valid input (even number of binary digits).

b) Mister Intal (Alice’s supervisor) is only familiar with assembler and the Turing machine. Therefore, Alice is now asked to translate her transducer to a Turing machine. The two numbers \( a \) and \( b \) are written on the machine tape in the form \( a[n]a[n-1] \ldots a[1]a[0] + b[m]b[m-1] \ldots b[1]b[0] \), where \( a[n] \) is the MSB of \( a \). Note that the two numbers are separated by the '+' symbol, that they have an equal number of bits\(^1\), i.e. \( m = n \), and that \( \Gamma = \{0|1|+|\square\} \). Initially, the head of the TM points to \( a[n] \). At the end, it should point to the MSB of the result.

Hint You may extend the alphabet \( \Gamma \) to put temporary symbols on the tape.

c) In her last assignment, Alice is asked to implement a \textit{binary to unary converter}. This converter takes a number \( a \) in binary (alphabet \( \{0|1\} \)) and converts it to a unary number \( u \) (alphabet \( \{1\} \)). Initially, the TM head points to the MSB of \( a \). At the end, the head should point to the right-most digit of \( u \).

\(^1\)Your machine may crash or produce a wrong result if this condition does not hold.