Specification models and their analysis -Background Material-

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Part I

Introduction

Motivation

- In the following we will develop a concise (mathematical) framework for formally describing systems of interest (→ formal model).
- This framework allows one to formally, i. e., mathematically reason about a model's and hence a system's correctness w. r. t. dedicated properties, e. g. deadlock-freeness etc.
- In principle we could start with any programming language. However, their interpretation is very complicated (address arithmetic, arbitrary data types, ...). Also only certain aspects of a system matter, where one may abstract away many details. Hence it appears useful to follow a more abstract view and speak here only about very simple "languages" for describing systems. Such methods are commonly denoted as high-level model description methods.

Motivation

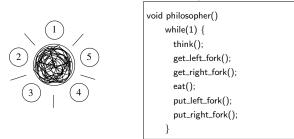
- Even though the high-level model description methods appear very simple, they possess clearly defined (execution or operational) semantics. These semantics allow us to map them to graphs.
- These graphs represent all possible behaviors of the specified high-level description.
- Hence the basic objects which represent the entities to be studied are graphs. Therefore we will briefly re-visit some basic definitions, which you probably have already seen before.
- Don't mind the formal notation, this will be made clear by examples and allows you to understand the resp. literature.

The dining philosophers Dijkstra'65 (en.wiki

There are N philosophers sitting around a circular table either thinking or eating pasta. Each philosopher needs his left and right fork to eat, but there is only one fork between each 2 philosophers. Design an algorithm that the philosophers can follow.

Consider the following protocol (= sequence of interaction)

pedia.org/wiki/Dining_ph



Properties: Deadlock? Starvation-free? Etc.

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Part II

Basic (formal) Facts about Graphs

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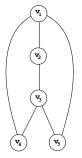
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(2)

(1)

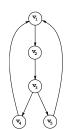
Preliminaries – Foundations of Graphs (at a glance)

Preliminaries – Foundations of Graphs (at a glance)



Definition 2.1: Graph

- A graph **G** is a pair (\mathbb{V}, \mathbb{E}) where
 - \mathbb{V} is a discrete set of vertices (or nodes) $\{v_1, \ldots, v_m\}$



Definition 2.2: Directed Graph (Digraph)

- If the elements of E are ordered in such a way that (v, w) ≠ (w, v) the elements of E are denoted as directed edges or directed arcs.
- A graph with such a property is denoted directed graph or *digraph* for short.

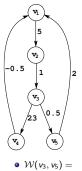
Preliminaries – Foundations of Graphs (at a glance)

- In a digraph and for an ordered pair (u, v) ∈ E vertex u is denoted as predecessor or parent of vertex v and vertex v is denoted as successor or child of vertex u.
- This can be extended to the level of sets of children and parent nodes as follows:
 - $\frac{\mathcal{P}ost(u) := \{ v \in \mathbb{V} \mid (u, v) \in \mathbb{E} \}}{\text{is the set of all children or direct successors of a vertex } u.}$
 - $\frac{\mathcal{P}re(v) := \{u \in \mathbb{V} \mid (u, v) \in \mathbb{E}\}}{\text{is the set of all parents or direct predecessors of a vertex } v.}$
- The above sets are very helpful, once we want to operate on graphs.

 \longrightarrow Question 2.1: Please write down an algorithm which check if a node n is contained in a graph

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Preliminaries – Foundations of Graphs (at a glance)



• $\mathcal{W}(v_3, v_1) =$

Definition 2.3: Weighted Graphs

- If the edges of a graph are labelled with elements from $\mathbb R$ one speaks of a *weighted graph*.
- In fact the set of edges of a weighted digraph is a ternary relation (set of triples): $\mathbb{E} \subseteq \mathbb{V} \times \mathbb{R} \times \mathbb{V}.$
- Function W : V × V → R gives one the weight associated with the respective edge (u, x, v): (u, x, v) ∈ E ⇒ W(u, v) = x and W(u, v) := 0 for all triples not contained in E.

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Preliminaries

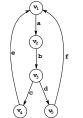
Check reachability of a node n

- (1) Nodes2beTraversed := { v_0 }, Nodes := \emptyset , Edges := \emptyset
- (2) While Nodes2beTraversed $\neq \emptyset$
- (3) $v_s := getElement(Nodes2beTraversed) //get node to be processed next$
- (4) Edges := PostSetEdges(v) //get set of outgoing edges of node v_s
 (5) While Edges ≠ Ø //still edges we did not travers so far?
- (6) while Loges + w //sum edges we did not travers so far?
 (6) e := pop(Edges)//pick one of the outgoing edges of node v_s
- (7) $v_t := get2ndElement(e) //extract children node w.r.t. edge e$
- (8) if $(v_t == n)$ return(YES)//did we reach node with the second seco
- (9) if $(v_t \notin Nodes) // did we reach a new node <math>v_t$ to be tra
 - versed; avoid being trapped in cycles.
- (10) $insert(v_t, Nodes) / / \text{ put } v_t \text{ in set of known states}$
- (11) $insert(v_t, Nodes2beTraversed) // put v_t in set of states to be traversed$
- (12) remove(e, Edges) //done with edge e
 (13) remove(v_s, Nodes2beTraversed) //done with node v_s
- (13) remove(vs, Node (14) return(NO)

Please clarify the functions PostSetEdges.

Order of traversal: depth-first-search or breadth first search?

Preliminaries – Foundations of Graphs (at a glance)



Definition 2.4: Labelled Graphs

 If the edges of a graph are labelled with elements from a finite set, e. g. *l* ∈ *Act* one speaks commonly of a *labelled graph*.

• In fact the set of edges of a labelled digraph is once again a ternary relation: $\mathbb{E} \subseteq \mathbb{V} \times Act \times \mathbb{V}$.

Note:

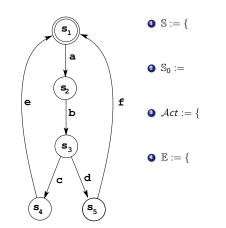
A labelled graph is also often refered to as labelled transition system (LTS), where instead of vertices one speaks of states.

Preliminaries – Labelled Transition System

Definition 2.5: Labelled Transition System (LTS)

A LTS is a quadruple $\mathcal{T}:=(\mathbb{S},\mathbb{S}_0,\mathcal{A}ct,\mathbb{E})$, where

- ${\small \bigcirc} \ {\mathbb S}:=\{\vec{s}_1,\ldots,\vec{s}_n\} \text{ is an ordered (indexed) set of states with}$
- 2 \mathbb{S}_0 as the set of initial states.
- Act is the discrete set of transition labels,
- $\mathbb{E} \subseteq \mathbb{S} \times Act \times \mathbb{S}$ is an ordered (indexed) set of labelled state-to-state transitions.



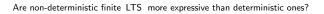
Preliminaries – Labelled Transition system

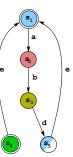
Preliminaries – Termination and Determinism of LTS

Note:

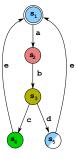
LTS are essentially the semantics of the here discussed high-level modelling techniques, where the techniques of model checking allow us to reason about their properties. In the following we briefly strive some important definitions.

- A LTS \mathcal{T} is defined *non-terminal* if each state has at least one out-going edge otherwise \mathcal{T} is called *terminal*.
- A LTS T is defined deterministic if each state has at most one out-going edge with the same edge label otherwise T is denoted as *non-deterministic*.





Preliminaries – Termination and Determinism (Examples)



non-terminal,deter

n-terminal, deterministic

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Backus-Naur-Form

BNF is a method for compactly writing down production rules (of a context-free grammar). The production rules employ variables (capital letters) and terminal symbols (lower case letters).

$$A ::= true \mid \alpha \in \mathcal{AP} \mid \neg A \mid A \land A \mid (A)$$

is read as follows: each occurence of variable A can be replaced by the constant *true*, a terminal symbol of set \mathcal{AP} or $\neg A$ or $A \land A$ or (A). One may note that A may not only be a non-terminal symbol (variable), it can also be a word produced by the above grammar. This is, because it also appears on the left-hand side.

 \longrightarrow Question 3.1: What does we above rule define?

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Part III

Backus-Naur-Form